Abstracts

A GaAs MMIC for a 2-GHz Successive Detection Logarithmic Amplifier

L.W. Chua. "A GaAs MMIC for a 2-GHz Successive Detection Logarithmic Amplifier." 1992 Microwave and Millimeter-Wave Monolithic Circuits Symposium Digest 92.1 (1992 [MCS]): 145-148.

This paper reports on a GaAs MMIC for a successive detection logarithmic amplifier which gives a logging linearity over the instantaneous bandwidth 2-7GHz of ±1.25dB (theory: ±1.1dB). It represents the best published results for both hybrid MIC and MMIC realisations, and the lowest chip count per stage, to date, for frequencies above 2GHz.

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